

**Amendments to the Claims:**

The following listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Currently Amended) An electro-optical device including a plurality of scanning lines and a plurality of data lines which are wired to cross the scanning lines, comprising:

an electrode which is wired to cross the data lines and is capacitively coupled with the data lines;

~~an inversion logic circuit, comprising an input terminal supplied with a predetermined level for a bias level, that compares signal levels generated in the electrode to the predetermined level to produce an output value; and~~  
circuit that includes an input terminal and inverts an input signal supplied to the input terminal to produce an output signal, the input signal being a signal generated at the electrode biased by a predetermined level, the output signal being a binary pulse whose width corresponds to a magnitude relationship of the signal generated at the electrode and the predetermined level; and

logic circuits that selectively adjust a signal supplied to each scanning line by one of two predetermined amounts based, in part, upon the output value produced by the inversion logic circuit.

2. (Currently Amended) An electro-optical device including:

a plurality of scanning lines;

a scanning line driving circuit that supplies to each of the scanning lines a scanning signal which is set to be at a selection level and a non-selection level corresponding to a selection period and a non-selection period of each scanning line;

a plurality of data lines which are wired to cross the scanning lines;

a data line driving circuit that supplies to each of the data lines a data signal whose pulse width is modulated on the basis of display data; and

pixels provided in portions where the scanning lines cross the data lines and driven on the basis of the scanning signals and the data signals,

the electro-optical device comprising:

an electrode which is wired to cross the data lines and is capacitively coupled with the data lines;

~~an inversion logic circuit, comprising an input terminal supplied with a predetermined level for a bias level, that compares signal levels generated in the electrode to the predetermined level to produce an output value; and~~  
circuit that includes an input terminal and inverts an input signal supplied to the input terminal to produce an output signal, the input signal being a signal generated at the electrode biased by a predetermined level, the output signal being a binary pulse whose width corresponds to a magnitude relationship of the signal generated at the electrode and the predetermined level; and

logic circuits that selectively adjust a signal supplied to each scanning line by one of two predetermined amounts based, in part, upon the output value produced by the inversion logic circuit.

3. (Canceled)

4. (Previously Presented) The electro-optical device according to Claim 2, the logic circuits not adjusting the signal supplied to each scanning line at an early state of the selection period to the selection level.

5. (Currently Amended) A method of driving an electro-optical device including a plurality of scanning lines, a scanning line driving circuit that supplies to each of the scanning lines a scanning signal which is set to be at a selection level and a non-selection level corresponding to a selection period and a non-selection period of each scanning line, a

plurality of data lines which are wired to cross the scanning lines, a data line driving circuit that supplies to each of the data lines a data signal whose pulse width is modulated on the basis of display data, and pixels provided in portions where the scanning lines cross the data lines and driven on the basis of the scanning signals and the data signals, the method comprising:

wiring an electrode to cross the data lines and capacitively coupling the electrode with the data lines;

~~comparing signal levels generated in the electrode to a predetermined level to produce an output value; and~~supplying an input signal to an input terminal of an inversion logic circuit, the input signal being a signal generated at the electrode biased by a predetermined level;

inverting the input signal to produce an output signal, the output signal being a binary pulse whose width corresponds to a magnitude relationship of the signal generated at the electrode and the predetermined level; and

selectively adjusting the signal supplied to each scanning line by one of two predetermined amounts based, in part, upon the output value.

6. (Currently Amended) A circuit for driving an electro-optical device including:

a plurality of scanning lines;

a scanning line driving circuit that supplies to each of the scanning lines a scanning signal which are set to be at a selection level and a non-selection level corresponding to a selection period and a non-selection period of each scanning line;

a plurality of data lines which are wired to cross the scanning lines;

a data line driving circuit that supplies to each of the data lines a data signal whose pulse width is modulated on the basis of display data, and pixels provided in portions

where the scanning lines cross the data lines and driven on the basis of the scanning signals and the data signals,

the circuit comprising an electrode which is wired to cross the data lines and is capacitively coupled with the data lines, and

~~the circuit comparing signal levels generated in the electrode to a~~  
~~predetermined level to produce an output value, and~~ an input terminal, an input signal  
supplied to the input terminal being inverted to produce an output signal, the input signal  
being a signal generated at the electrode biased by a predetermined level, and the output  
signal being a binary pulse whose width corresponds to a magnitude relationship of the signal  
generated at the electrode and the predetermined level;

the circuit selectively adjusting the signal supplied to each scanning line by one of two predetermined amounts based, in part, upon the output value.

7. (Original) An electronic apparatus, comprising the electro-optical device according to Claim 1.

8. (Previously Presented) The electro-optical device according to Claim 1, wherein the output value generated by the inversion logic circuit is high if a signal level generated in the electrode is lower than the predetermined level and is low if a signal level generated in the electrode is higher than the predetermined level.

9. (Previously Presented) The electro-optical device according to Claim 2, wherein the output value generated by the inversion logic circuit is high if a signal level generated in the electrode is lower than the predetermined level and is low if a signal level generated in the electrode is higher than the predetermined level.

10. (Previously Presented) The method according to Claim 5, wherein the signal levels generated in the electrode are compared to a predetermined level to produce an output value that is high if a signal level generated in the electrode is lower than the predetermined

level and to produce an output value that is low if a signal level generated in the electrode is higher than the predetermined level.

11. (Previously Presented) The circuit according to Claim 6, wherein the circuit compares signal levels generated in the electrode to a predetermined level to produce an output value that is high if a signal level generated in the electrode is lower than the predetermined level and to produce an output value that is low if a signal level generated in the electrode is higher than the predetermined level.